INFORMATION DISCLOSURE CITATION IN AN APPLICATION					ATTY. DOCKET NO. 67161-103	SE	SERIAL NO. 10/665,010			
					APPLICANT Tadaaki YAMAUCHI					
(PTO-1449)					FILING DATE September 18, 2003 GROUP 28/8					
U.S. PATENT DOCUMENTS										
EXAMINER'S		Document Number		Publication Date	Name of Patentee or Applicant of Cited		T	Pages, Columns, Lines, Where		
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AM	"A 1-Mbit CMOS EPROM with Enhanced Verification", Roberto Gastaldl et al., IEEE Journal of Solid-State Circuits, Vol. 23, No. 5, October 1988, pp. 1150-1158									
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